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retargeting AND kernel	27

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<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	DB=PGPB; PLUR=NO; OP=OR		
<u>L6</u>	retargeting AND kernel	27	<u>L6</u>
	DB=USPT; PLUR=NO; OP=OR		
<u>L5</u>	L4 and kernel	3	<u>L5</u>
<u>L4</u>	L3 OR L2	13	<u>L4</u>
<u>L3</u>	L1 and retargeting	12	<u>L3</u>
<u>L2</u>	L1 AND retarget	2	<u>L2</u>
<u>L1</u>	(717/136 717/146 717/162).ccls. OR (703/23 703/24 703/25 703/26 703/27 703/28).ccls. OR (716/18).ccls.	2644	<u>L1</u>

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☐ 1. Document ID: US 20050226406 A1

L6: Entry 1 of 27

File: PGPB

Oct 13, 2005

PGPUB-DOCUMENT-NUMBER: 20050226406

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050226406 A1

TITLE: Efficient linking and loading for late binding and platform retargeting

PUBLICATION-DATE: October 13, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Forin, Alessandro	Bellevue	WA	US
Helander, Johannes V.	Bellevue	WA	US

US-CL-CURRENT: 380/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Drawings
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☐ 2. Document ID: US 20050223018 A1

L6: Entry 2 of 27

File: PGPB

Oct 6, 2005

PGPUB-DOCUMENT-NUMBER: 20050223018

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050223018 A1

TITLE: Efficient linking and loading for late binding and platform retargeting

PUBLICATION-DATE: October 6, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Forin, Alessandro	Bellevue	WA	US
Helander, Johannes V.	Bellevue	WA	US

US-CL-CURRENT: 707/100

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Drawings
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☐ 3. Document ID: US 20050216701 A1

L6: Entry 3 of 27

File: PGPB

Sep 29, 2005

PGPUB-DOCUMENT-NUMBER: 20050216701
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050216701 A1

TITLE: Automatic configuration of a microprocessor

PUBLICATION-DATE: September 29, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Taylor, Richard Michael	Midlothian		GB

US-CL-CURRENT: 712/34; 712/209, 712/227

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 4. Document ID: US 20050193232 A1

L6: Entry 4 of 27

File: PGPB

Sep 1, 2005

PGPUB-DOCUMENT-NUMBER: 20050193232
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050193232 A1

TITLE: System and method for persistent RAM disk

PUBLICATION-DATE: September 1, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Diehl, Shawn T.	Mount Vernon	NH	US

US-CL-CURRENT: 714/5

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 5. Document ID: US 20050169932 A1

L6: Entry 5 of 27

File: PGPB

Aug 4, 2005

PGPUB-DOCUMENT-NUMBER: 20050169932
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050169932 A1

TITLE: Uses of monoclonal antibody 8h9

PUBLICATION-DATE: August 4, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Cheung, Nai-Kong V	Purchase	NY	US

US-CL-CURRENT: 424/160.1; 435/320.1, 435/339.1, 435/69.1, 530/388.3, 536/23.53

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Drawings
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☐ 6. Document ID: US 20050027871 A1

L6: Entry 6 of 27

File: PGPB

Feb 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050027871

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050027871 A1

TITLE: Interoperable systems and methods for peer-to-peer service orchestration

PUBLICATION-DATE: February 3, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Bradley, William	Newark	DE	US
Maher, David	Livermore	CA	US
Boccon-Gibod, Gilles	Los Altos	CA	US

US-CL-CURRENT: 709/227

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Drawings
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☐ 7. Document ID: US 20050015384 A1

L6: Entry 7 of 27

File: PGPB

Jan 20, 2005

PGPUB-DOCUMENT-NUMBER: 20050015384

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050015384 A1

TITLE: Relocation of metadata server with outstanding DMAPI requests

PUBLICATION-DATE: January 20, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Wehrman, Geoffrey	Minneapolis	MN	US
Roehrich, Dean	Eagan	MN	US

US-CL-CURRENT: 707/100

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Draw D
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☐ 8. Document ID: US 20040261145 A1

L6: Entry 8 of 27

File: PGPB

Dec 23, 2004

PGPUB-DOCUMENT-NUMBER: 20040261145

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040261145 A1

TITLE: Genetic transformation in plants using site-specific recombination and wide hybridization

PUBLICATION-DATE: December 23, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Lyznik, Leszek Alexander	Johnston	IA	US
Baszczyński, Christopher L.	Urbandale	IA	US
Orczyk, Wacław	Blonie		PL

US-CL-CURRENT: 800/260; 800/266, 800/278

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMC	Draw D
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☐ 9. Document ID: US 20040250231 A1

L6: Entry 9 of 27

File: PGPB

Dec 9, 2004

PGPUB-DOCUMENT-NUMBER: 20040250231

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040250231 A1

TITLE: Automated processor generation system for designing a configurable processor and method for the same

PUBLICATION-DATE: December 9, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Killian, Earl A.	Los Altos Hills	CA	US
Gonzalez, Ricardo E.	Menlo Park	CA	US
Dixit, Ashish B.	Mt. View	CA	US
Lam, Monica	Menlo Park	CA	US
Lichtenstein, Walter D.	Belmont	MA	US
Rowen, Christopher	Santa Cruz	CA	US
Ruttenberg, John C.	Newton	MA	US
Wilson, Robert P.	Palo Alto	CA	US
Wang, Albert Ren-Rui	Fremont	CA	US

Maydan, Droe Eliezer

Palo Alto

CA

US

US-CL-CURRENT: 716/18

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 10. Document ID: US 20040250113 A1

L6: Entry 10 of 27

File: PGPB

Dec 9, 2004

PGPUB-DOCUMENT-NUMBER: 20040250113

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040250113 A1

TITLE: Clustered filesystem for mix of trusted and untrusted nodes

PUBLICATION-DATE: December 9, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Beck, Kenneth S.	Morgan Hill	CA	US

US-CL-CURRENT: 726/27

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 11. Document ID: US 20040249904 A1

L6: Entry 11 of 27

File: PGPB

Dec 9, 2004

PGPUB-DOCUMENT-NUMBER: 20040249904

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040249904 A1

TITLE: Multi-class heterogeneous clients in a clustered filesystem

PUBLICATION-DATE: December 9, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Moore, Daniel	North Carlton		AU
Gildfind, Andrew	Richmond		AU

US-CL-CURRENT: 709/216

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 12. Document ID: US 20040243544 A1

PGPUB-DOCUMENT-NUMBER: 20040243544
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040243544 A1

TITLE: Dynamic symbolic link resolution

PUBLICATION-DATE: December 2, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Hipp, Burton A.	Elk Grove	CA	US

US-CL-CURRENT: 707/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 13. Document ID: US 20040210673 A1

L6: Entry 13 of 27

File: PGPB

Oct 21, 2004

PGPUB-DOCUMENT-NUMBER: 20040210673
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040210673 A1

TITLE: Messaging between heterogeneous clients of a storage area network

PUBLICATION-DATE: October 21, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Cruciani, Mark	Lakeville	MN	US
Beck, Kenneth S.	Morgan Hill	CA	US

US-CL-CURRENT: 709/246; 709/230

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 14. Document ID: US 20040210656 A1

L6: Entry 14 of 27

File: PGPB

Oct 21, 2004

PGPUB-DOCUMENT-NUMBER: 20040210656
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040210656 A1

TITLE: Failsafe operation of storage area network

PUBLICATION-DATE: October 21, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Beck, Kenneth S.	Morgan Hill	CA	US
Goodwin, Mark J.	Selby		AU

US-CL-CURRENT: 709/225

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RMBC	Grand D.
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☐ 15. Document ID: US 20040153841 A1

L6: Entry 15 of 27

File: PGPB

Aug 5, 2004

PGPUB-DOCUMENT-NUMBER: 20040153841
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040153841 A1

TITLE: Failure hierarchy in a cluster filesystem

PUBLICATION-DATE: August 5, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Beck, Ken	Morgan Hill	CA	US

US-CL-CURRENT: 714/42

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RMBC	Grand D.
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☐ 16. Document ID: US 20040143607 A1

L6: Entry 16 of 27

File: PGPB

Jul 22, 2004

PGPUB-DOCUMENT-NUMBER: 20040143607
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040143607 A1

TITLE: Recovery and relocation of a distributed name service in a cluster filesystem

PUBLICATION-DATE: July 22, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Beck, Ken	Morgan Hill	CA	US

US-CL-CURRENT: 707/201

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RMBC	Grand D.
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☐ 17. Document ID: US 20040139125 A1

L6: Entry 17 of 27

File: PGPB

Jul 15, 2004

PGPUB-DOCUMENT-NUMBER: 20040139125
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040139125 A1

TITLE: Snapshot copy of data volume during data access

PUBLICATION-DATE: July 15, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Strassburg, Roger	Oberpfammern	MN	DE
Overby, Glen	Apple Valley	MN	US
Lord, Stephen Philip	Prior Lake	NY	US
Costello, Laurie	Painted Post	MN	US
Bannister, Neil	Eagan		US

US-CL-CURRENT: 707/202

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 18. Document ID: US 20040071317 A1

L6: Entry 18 of 27

File: PGPB

Apr 15, 2004

PGPUB-DOCUMENT-NUMBER: 20040071317
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040071317 A1

TITLE: Method for visual tracking using switching linear dynamic system models

PUBLICATION-DATE: April 15, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Pavlovie, Vladimir	Melrose	MA	US
Rehg, James Matthew	Arlington	MA	US

US-CL-CURRENT: 382/103

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 19. Document ID: US 20040042639 A1

L6: Entry 19 of 27

File: PGPB

Mar 4, 2004

PGPUB-DOCUMENT-NUMBER: 20040042639
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040042639 A1

TITLE: Method for motion classification using switching linear dynamic system models

PUBLICATION-DATE: March 4, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Pavlovic, Vladimir	Melrose	MA	US
Rehg, James Matthew	Arlington	MA	US

US-CL-CURRENT: 382/107; 382/224

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 20. Document ID: US 20030208723 A1

L6: Entry 20 of 27

File: PGPB

Nov 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030208723
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030208723 A1

TITLE: Automated processor generation system for designing a configurable processor and method for the same

PUBLICATION-DATE: November 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Killian, Earl A.	Los Altos Hills	CA	US
Gonzalez, Ricardo E.	Menlo Park	CA	US
Dixit, Ashish B.	Mountain View	CA	US
Lam, Monica	Menlo Park	CA	US
Lichtenstein, Walter D.	Belmont	MA	US
Rowen, Christopher	Santa Cruz	CA	US
Ruttenberg, John C.	Newton	MA	US
Wilson, Robert P.	Palo Alto	CA	US
Wang, Albert Ren-Rui	Fremont	CA	US
Maydan, Dror Eliezer	Palo Alto	CA	US

US-CL-CURRENT: 716/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	FIGS	Drawings
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☐ 21. Document ID: US 20030103963 A1

PGPUB-DOCUMENT-NUMBER: 20030103963
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030103963 A1

TITLE: Uses of monoclonal antibody 8H9

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Cheung, Nai-Kong V.	Purchase	NY	US

US-CL-CURRENT: 424/130.1; 424/141.1, 435/320.1, 435/326, 435/69.1, 530/388.15,
536/23.53

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMBO	Drawings
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☐ 22. Document ID: US 20030078946 A1

L6: Entry 22 of 27

File: PGPB

Apr 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030078946
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030078946 A1

TITLE: Clustered filesystem

PUBLICATION-DATE: April 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Costello, Laurie	Painted Post	NY	US
Mowat, Eric	Pledmont	CA	US
Leong, James	Hillsborough	CA	US

US-CL-CURRENT: 707/201

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	EMBO	Drawings
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☐ 23. Document ID: US 20030028514 A1

L6: Entry 23 of 27

File: PGPB

Feb 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030028514
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030028514 A1

TITLE: Extended attribute caching in clustered filesystem

PUBLICATION-DATE: February 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Lord, Stephen Philip	Prior Lake	MN	US
Kierski, Robert	Farmington	MN	US

US-CL-CURRENT: 707/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	WWW	Drawings
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☐ 24. Document ID: US 20020181017 A1

L6: Entry 24 of 27

File: PGPB

Dec 5, 2002

PGPUB-DOCUMENT-NUMBER: 20020181017
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020181017 A1

TITLE: Open coventuring in a remote hardcopy proofing service, with preserved clientele, through interface sharing

PUBLICATION-DATE: December 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Such, Alberto	Barcelona		ES
Mackenzie, Andrew	Barcelona		ES
Gonzalez, Andres	Barcelona		ES

US-CL-CURRENT: 358/1.15; 358/1.9, 358/402

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	WWW	Drawings
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☐ 25. Document ID: US 20020178432 A1

L6: Entry 25 of 27

File: PGPB

Nov 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020178432
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020178432 A1

TITLE: Method and system for synthesizing a circuit representation into a new circuit representation having greater unateness

PUBLICATION-DATE: November 28, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Kim, Hyungwon	San Jose	CA	US

Hayes, John P.

Ann Arbor

MI

US

US-CL-CURRENT: 716/18

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	Keywords	Drawings
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☐ 26. Document ID: US 20020102264 A1

L6: Entry 26 of 27

File: PGPB

Aug 1, 2002

PGPUB-DOCUMENT-NUMBER: 20020102264

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020102264 A1

TITLE: Uses of monoclonal antibody 8H9

PUBLICATION-DATE: August 1, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Cheung, Nai-Kong V.	Purchase	NY	US

US-CL-CURRENT: 424/155.1; 424/178.1, 435/326, 530/389.1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	Keywords	Drawings
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☐ 27. Document ID: US 20020023278 A1

L6: Entry 27 of 27

File: PGPB

Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020023278

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020023278 A1

TITLE: Genetic transformation in plants using site-specific recombination and wide hybridization

PUBLICATION-DATE: February 21, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Lyznik, Leszek Alexander	Johnston	IA	US
Baszczyński, Christopher L.	Urbandale	IA	US
Orczyk, Wacław	Blonie		PL

US-CL-CURRENT: 800/278; 800/260, 800/288, 800/320, 800/320.1, 800/320.2, 800/320.3

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	Keywords	Drawings
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Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 6760888 B2

L5: Entry 1 of 3

File: USPT

Jul 6, 2004

US-PAT-NO: 6760888

DOCUMENT-IDENTIFIER: US 6760888 B2

TITLE: Automated processor generation system for designing a configurable processor and method for the same

DATE-ISSUED: July 6, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Killian; Earl A.	Los Altos Hills	CA		
Gonzalez; Ricardo E.	Menlo Park	CA		
Dixit; Ashish B.	Mountain View	CA		
Lam; Monica	Menlo Park	CA		
Lichtenstein; Walter D.	Belmont	MA		
Rowen; Christopher	Santa Cruz	CA		
Ruttenberg; John C.	Newton	MA		
Wilson; Robert P.	Palo Alto	CA		
Wang; Albert Ren-Rui	Fremont	CA		
Maydan; Dror Eliezer	Palo Alto	CA		

US-CL-CURRENT: 716/1; 712/1, 712/200, 712/32, 712/35, 712/36, 716/18

ABSTRACT:

An automated processor design tool uses a description of customized processor instruction set extensions in a standardized language to develop a configurable definition of a target instruction set, a Hardware Description Language description of circuitry necessary to implement the instruction set, and development tools such as a compiler, assembler, debugger and simulator which can be used to develop applications for the processor and to verify it. Implementation of the processor circuitry can be optimized for various criteria such as area, power consumption, speed and the like. Once a processor configuration is developed, it can be tested and inputs to the system modified to iteratively optimize the processor implementation. By providing a constrained domain of extensions and optimizations, the process can be automated to a high degree, thereby facilitating fast and reliable development.

8 Claims, 15 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 14

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	FIGS	Drawings
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☐ 2. Document ID: US 6477683 B1

15: Entry 2 of 3

File: USPT

Nov 5, 2002

US-PAT-NO: 6477683

DOCUMENT-IDENTIFIER: US 6477683 B1

TITLE: Automated processor generation system for designing a configurable processor and method for the same

DATE-ISSUED: November 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Killian; Earl A.	Los Altos Hills	CA		
Gonzalez; Ricardo E.	Menlo Park	CA		
Dixit; Ashish B.	Mountain View	CA		
Lam; Monica	Menlo Park	CA		
Lichtenstein; Walter D.	Belmont	MA		
Rowen; Christopher	Santa Cruz	CA		
Ruttenberg; John C.	Newton	MA		
Wilson; Robert P.	Palo Alto	CA		
Wang; Albert Ren-Rui	Fremont	CA		
Maydan; Dror Eliezer	Palo Alto	CA		

US-CL-CURRENT: 716/1; 716/18

ABSTRACT:

An automated processor design tool uses a description of customized processor instruction set extensions in a standardized language to develop a configurable definition of a target instruction set, a Hardware Description Language description of circuitry necessary to implement the instruction set, and development tools such as a compiler, assembler, debugger and simulator which can be used to develop applications for the processor and to verify it. Implementation of the processor circuitry can be optimized for various criteria such as area, power consumption, speed and the like. Once a processor configuration is developed, it can be tested and inputs to the system modified to iteratively optimize the processor implementation. By providing a constrained domain of extensions and optimizations, the process can be automated to a high degree, thereby facilitating fast and reliable development.

104 Claims, 15 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 12

☐ 3. Document ID: US 6219828 B1

L5: Entry 3 of 3

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US-PAT-NO: 6219828

DOCUMENT-IDENTIFIER: US 6219828 B1

TITLE: Method for using two copies of open firmware for self debug capability

DATE-ISSUED: April 17, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lee; Van Hoa	Cedar Park	TX		

US-CL-CURRENT: 717/129; 711/102, 711/103, 711/104, 713/2, 714/100, 714/30, 717/136

ABSTRACT:

A first copy of Open Firmware is loaded into system memory to supply a debug function and a second copy of the same firmware is then loaded to provide functional code which is to be debugged. The first copy of Open Firmware in system memory is designated as the resident debugging function. Kernel code, within the first copy, sets up an executing environment for the debugger, such as system exception handlers and debug console enablement. Normal Open Firmware configuration variables are retrieved from Non-Volatile Random Access Memory ("NVRAM") by the first copy and transmitted to the loader. The second copy of Open Firmware is loaded into system memory to a location specified by the configuration variables. The second copy firmware image is designated as a normal Open Firmware operation in the system. The second copy initially takes over all system exception handlers except instruction breakpoint exception, program interrupt exception and trace exception. The instruction breakpoint exception is utilized to invoke the first copy, or resident debugger, from the normal Open Firmware (second copy) image during code debugging. The two copy debugging configuration is utilized in conjunction with an online machine language assembler and disassembler.

17 Claims, 4 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 4

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